## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Su-Jin Ahn et al. Serial No.: 10/696,615

Group Art Unit: 2811 Examiner: Sara W. Crane Confirmation No.: 6846

Filed: October 28, 2003 For: SEMICONDUCT

SEMICONDUCTOR MEMORY DEVICES HAVING OFFSET TRANSISTORS

February 12, 2007

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **RESPONSE TO NOVEMBER 15, 2006 OFFICE ACTION**

Sir:

Applicants provide the present submission in response to the Office Action mailed November 15, 2006. It is not believed that an extension of time and/or additional fee(s), including fees for net addition of claims, are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.